

FIG. 1

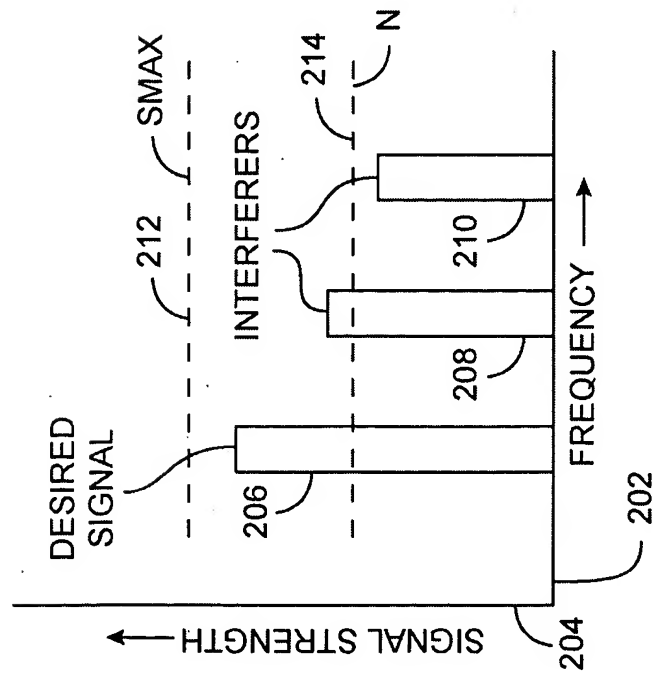


FIG. 2A

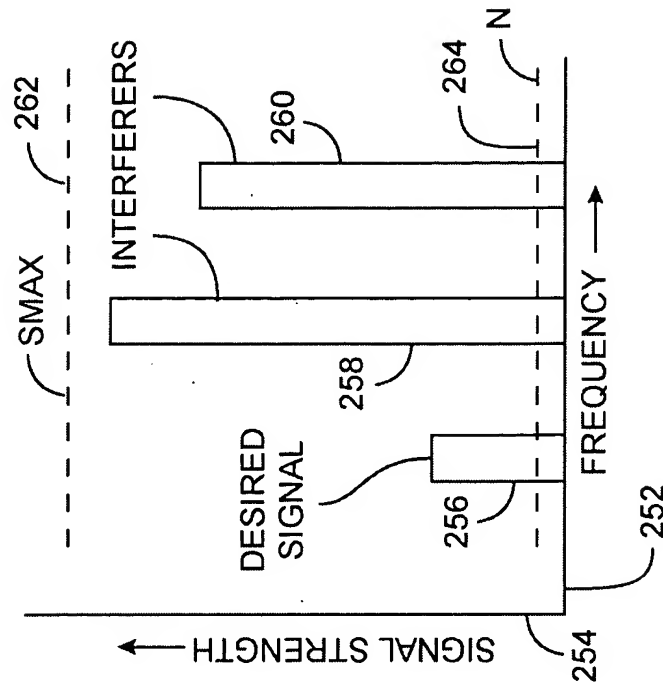


FIG. 2B

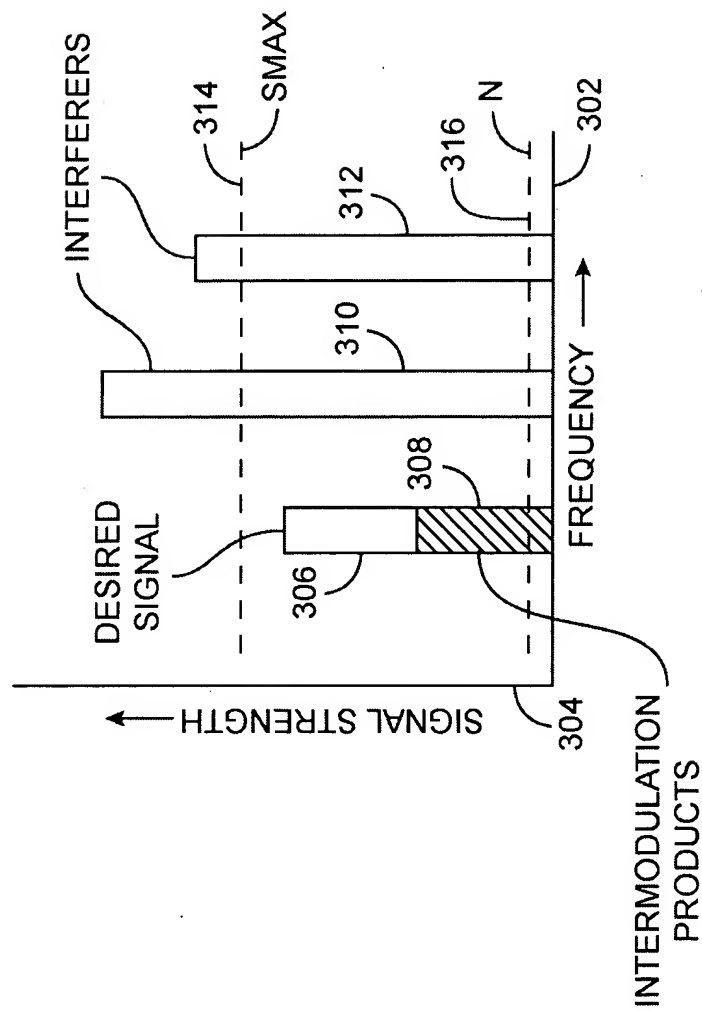


FIG. 3

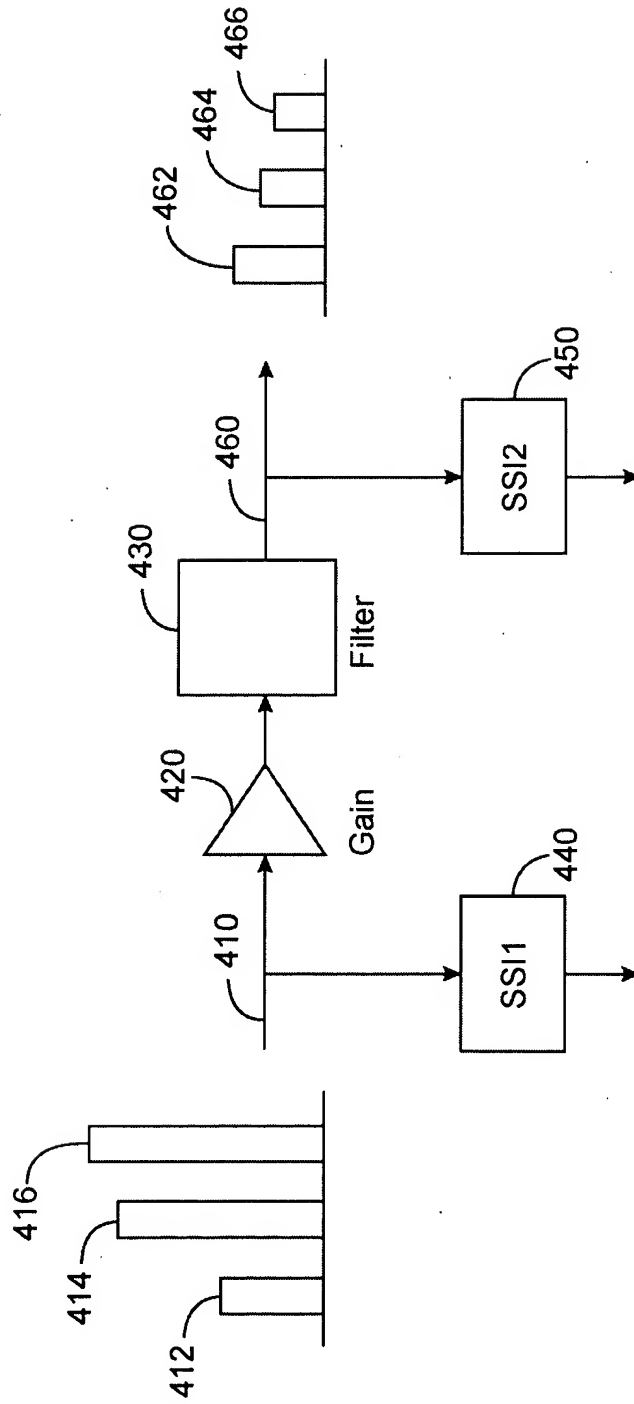


FIG. 4

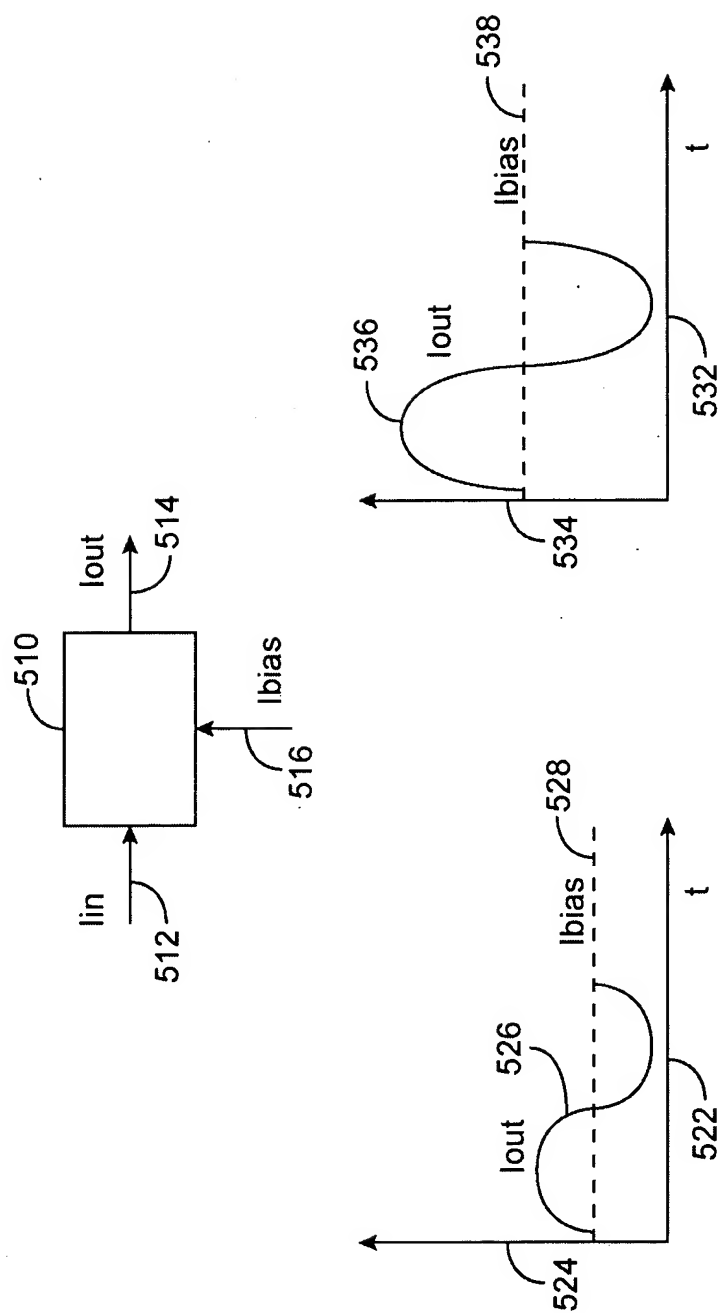


FIG. 5

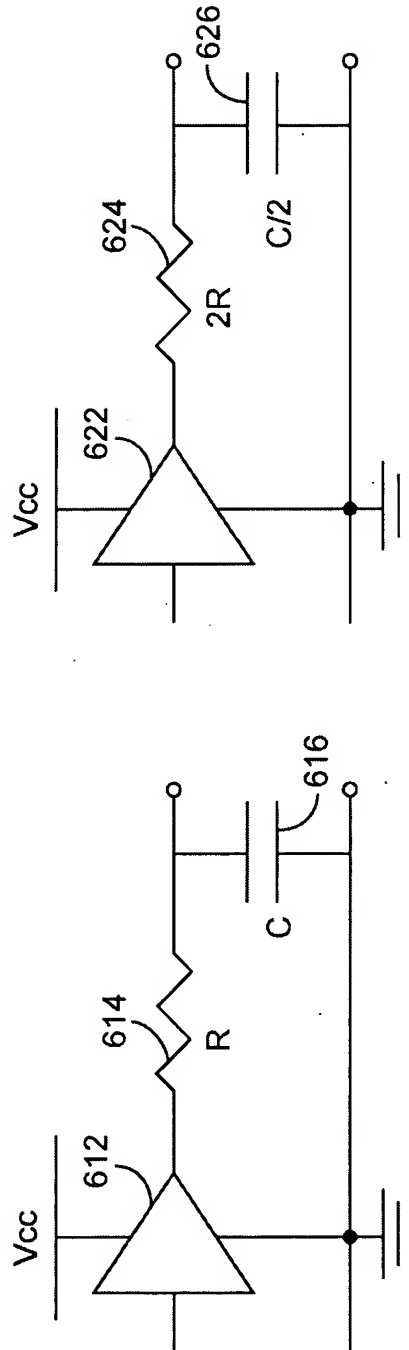


FIG. 6

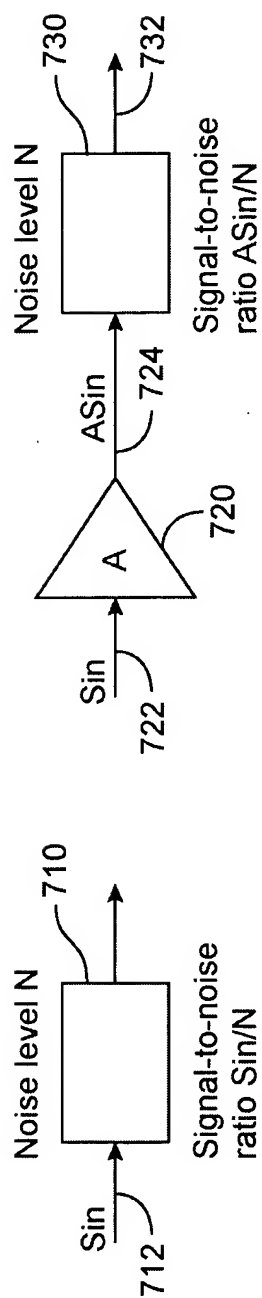


FIG. 7

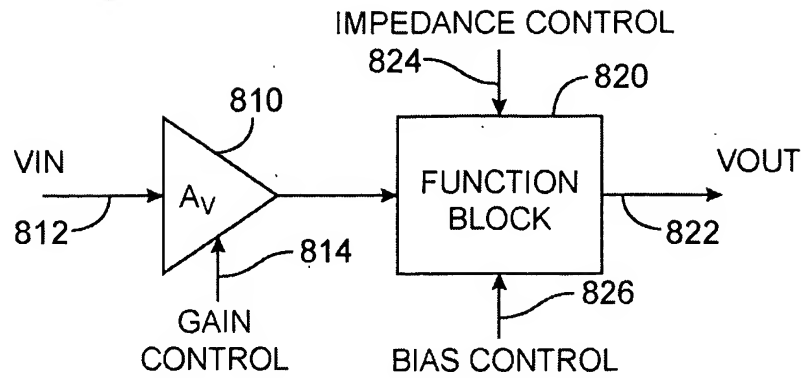


FIG. 8A

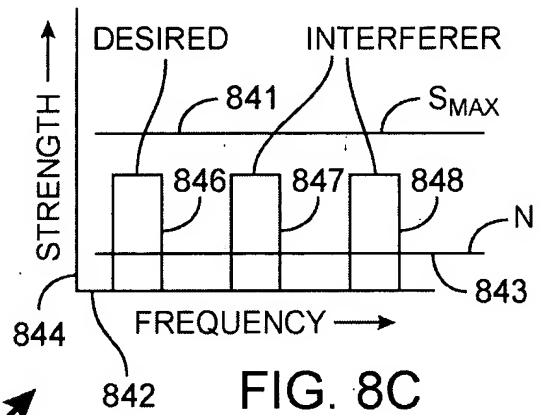


FIG. 8C

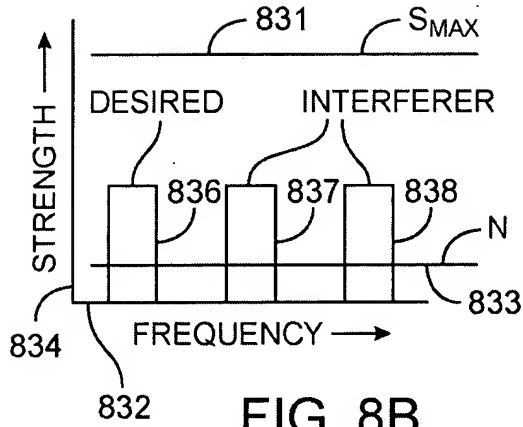


FIG. 8B

DECREASE
BIASING
- AND/OR -

INCREASE GAIN AND IMPEDANCES

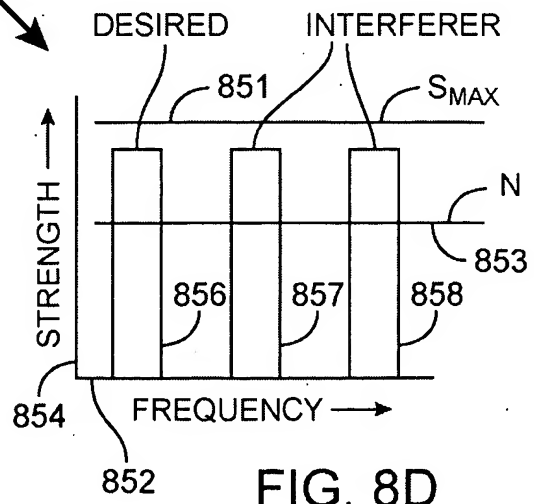


FIG. 8D

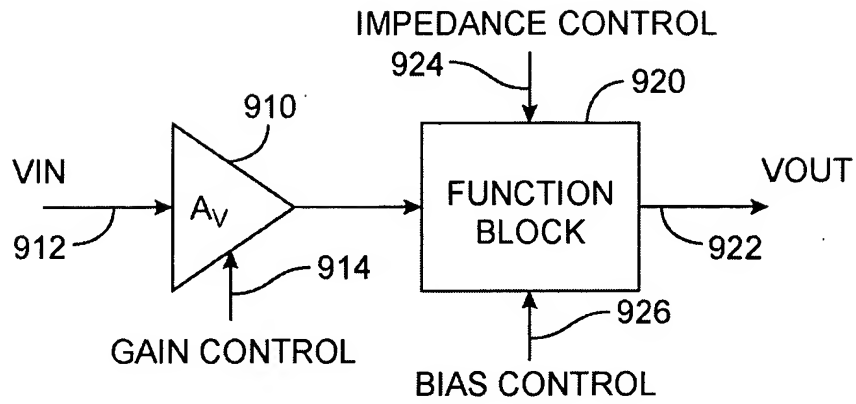


FIG. 9A

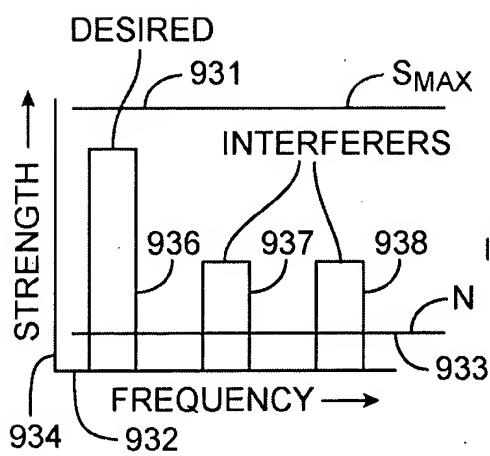


FIG. 9B

INCREASE
IMPEDANCE

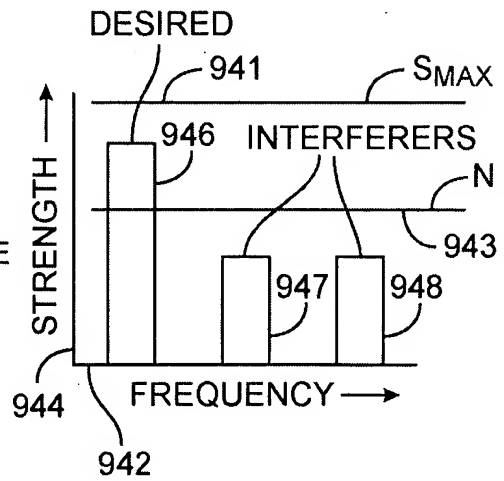


FIG. 9C

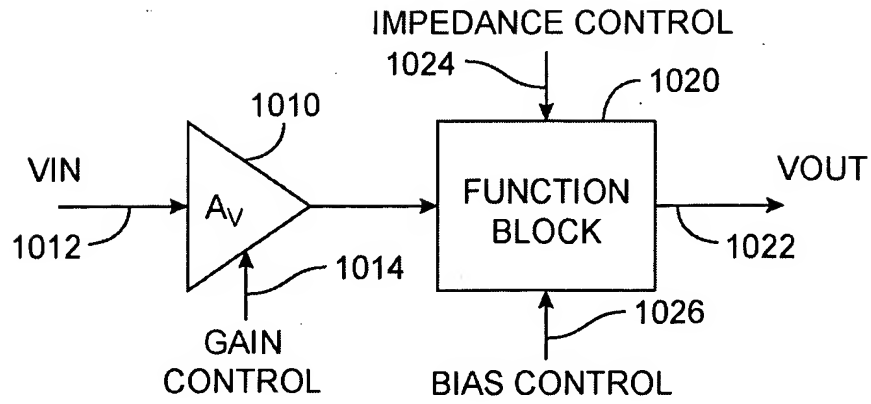


FIG. 10A

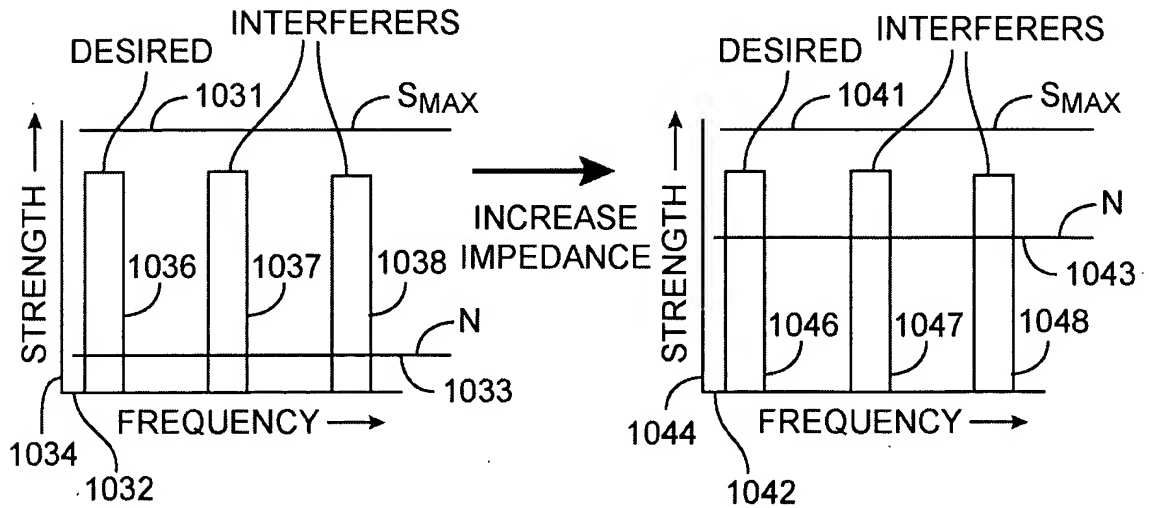


FIG. 10B

FIG. 10C

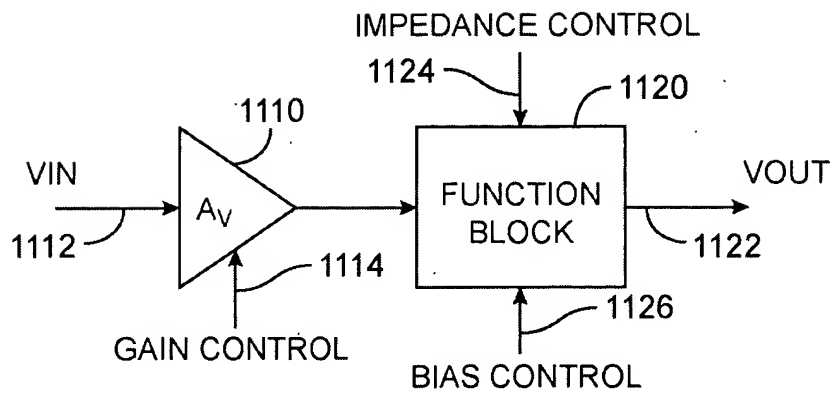


FIG. 11A

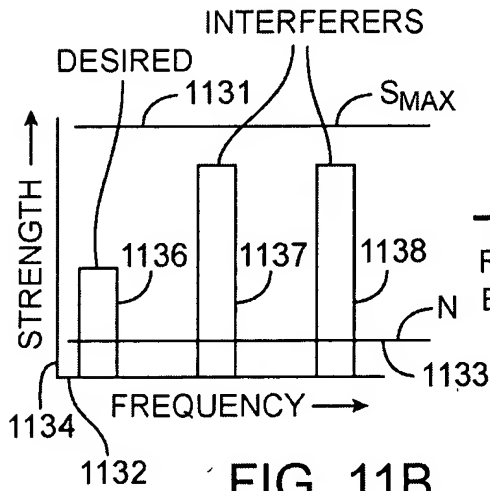


FIG. 11B

1160
REDUCE
BIASING

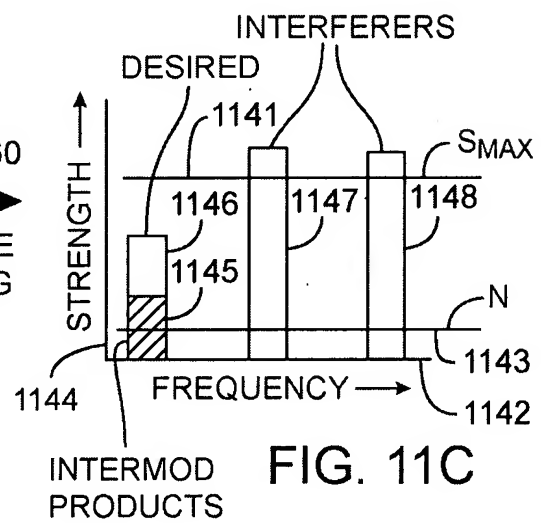


FIG. 11C

INTERMOD
PRODUCTS

- AND/OR -

1170
INCREASE
IMPEDANCE

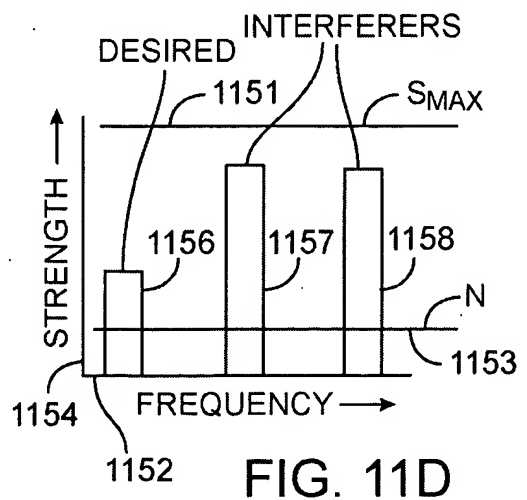


FIG. 11D

RECEIVED SIGNAL 1210		RESPONSE TO RECEIVED SIGNAL 1220		
SIGNAL STRENGTH	INTERFERER STRENGTH	DECREASE S _{MAX}	INCREASE IMPEDANCE NOT GAIN	INCREASE IMPEDANCE AND GAIN
1230 SMALL	SMALL	YES	NO	YES
1240 LARGE	SMALL	NO	YES	NO
1250 LARGE	LARGE	NO	YES	NO
1260 SMALL	LARGE	NO	NO	NO

FIG. 12

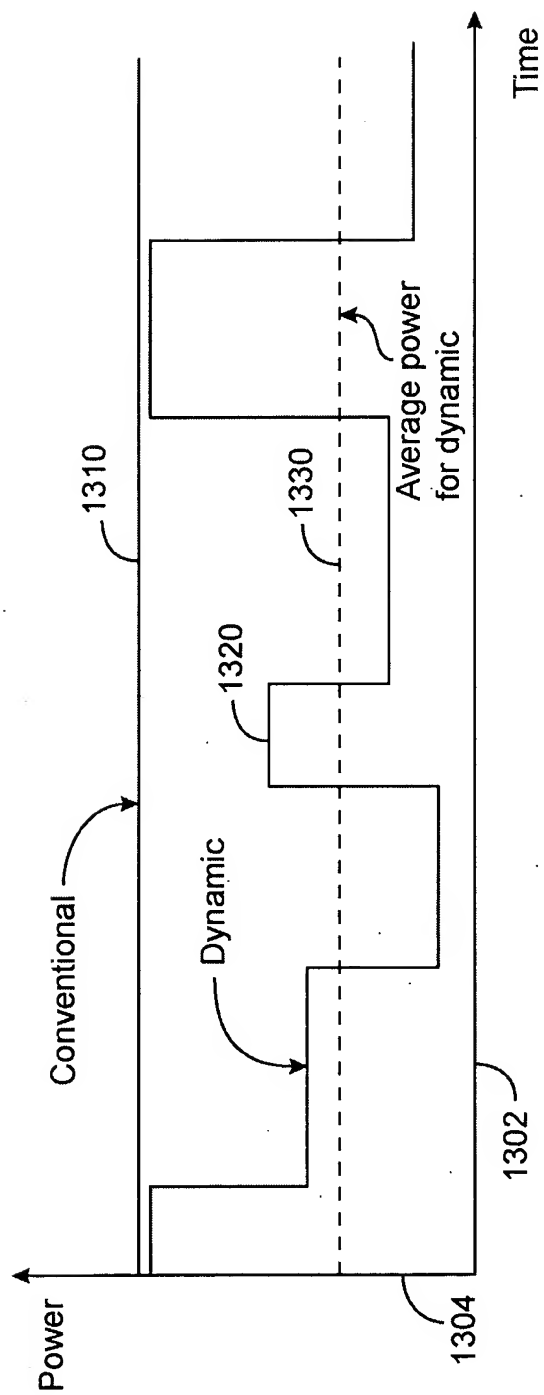


FIG. 13

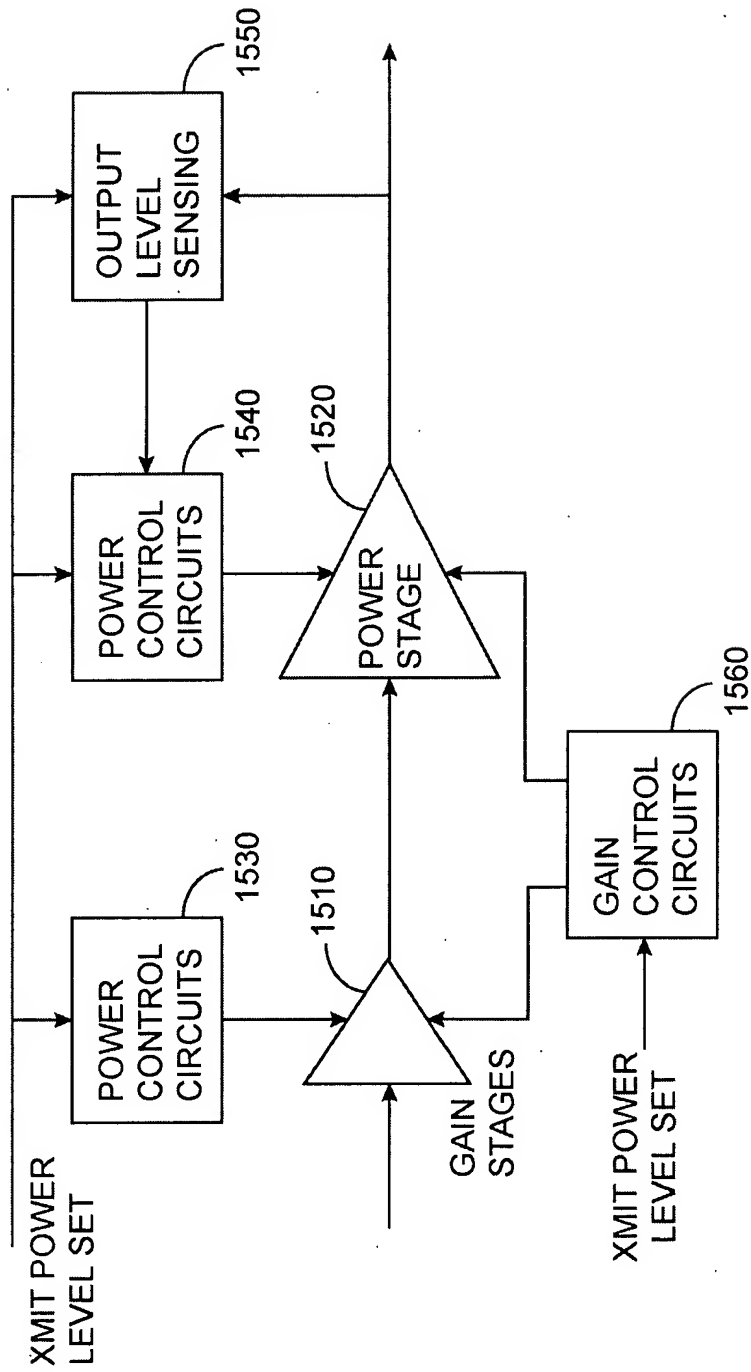


FIG. 15